Dale John Shidla, et al.

This Information Disclosure Statement is submitted:

() under 37 CFR 1.97 (d) together with a:

() under 37 CFR 1.97 (c) together with either a: () Statement under 37 CFR 1.97(e), or () a \$180.00 fee under 37 CFR 1.17(p), or

under 37 CFR 1.97(b), or

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Inventor(s):

Filing Date:

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Alexandria, VA 22313-1450

PATENT APPLICATION

ATTORNEY DOCKET NO. 200310483-1

Confirmati n No.:

Group Art Unit:

Examiner: not yet known

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT

(Within three months of filing national application; or date of entry of national application; or before

(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)

COMPILER-SCHEDULED CPU FUNCTIONAL TESTING

mailing date of first office action on the merits; whichever occurs last)

| () Statement under 37 CFR 1.97(e)(() a \$180.00 fee set forth in 37 CFR (Filed after final action, a notice of allowan | l 1.17(ρ). |
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| Please charge to Deposit Account 08-2025 pendency of this application, please charge any Account 08-2025 pursuant to 37 CFR 1.25. | the sum of \$0.00 . At any time during the fees required or credit any overpayment to Deposit |
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